



Docket No.: 1592-0169PUS1  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:  
Takashi OZAKI et al.

Patent No.: 7,534,730 B2

Issued: May 19, 2009

For: PRODUCING METHOD OF  
SEMICONDUCTOR DEVICE AND  
SUBSTRATE PROCESSING APPARATUS

**REQUEST FOR CERTIFICATE OF CORRECTION  
PURSUANT TO 37 CFR 1.323**

Attention: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Madam:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

On the Cover Page, under (73) Assignee: "Hitachi Kokusai Electric In., Tokyo (JP)" should be changed to read --Hitachi Kokusai Electric **Inc.**, Tokyo (JP)--.

The error was found in the application as filed by applicant. Please charge our Deposit Account No. 02-2448 in the amount of \$230.00 covering the fees set forth in 37 CFR 1.20(a) and 1.17(i).

The error now sought to be corrected is an inadvertent typographical error the correction of which does not involve new matter or require reexamination.

06/02/2009 MGE BREM1 00000021 022448 7534730  
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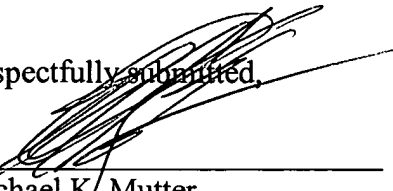
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Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to our Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Dated: June 1, 2009

Respectfully submitted,

By   
Michael K. Mutter  
Registration No.: 29,680  
BIRCH, STEWART, KOLASCH & BIRCH, LLP  
8110 Gatehouse Road  
Suite 100 East  
P.O. Box 747  
Falls Church, Virginia 22040-0747  
(703) 205-8000  
Attorney for Applicant

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CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,534,730 B2  
APPLICATION NO. : 10/549,938  
ISSUE DATE : May 19, 2009  
INVENTOR(S) : Takashi OZAKI et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**(73) Assignee: Hitachi Kokusai Electric Inc., Tokyo (JP)**

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Michael K. Mutter  
BIRCH, STEWART, KOLASCH & BIRCH, LLP 1  
8110 Gatehouse Road  
Suite 100 East  
P.O. Box 747  
Falls Church, Virginia 22040-0747